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Grace Alfcea



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Date: December 4, 2002

Bullis, et al.

Serial No. 09/409,940

Group Art Unit: 2123

Filed: September 30, 1999

Examiner: Ferris III, F.

For: METHOD AND SYSTEM FOR PROVIDING HIERARCHICAL  
SELF-CHECKING IN ASIC SIMULATION

Assistant Commissioner for Patents  
Washington, D.C. 20231

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Technology Center 2100

AMENDMENT

Sir:

In response to the Office Action dated July 12, 2002, please amend the above-identified application in the following manner:

**IN THE SPECIFICATION:**

Page 9, line 4, please insert:

Figure 7A is a flow chart depicting one embodiment of a method in accordance with the present invention performed by one embodiment of a snooper in accordance with the present invention.

Figure 7B is a flow chart depicting one embodiment of a method in accordance with the present invention performed by one embodiment of a checker in accordance with the present invention.